

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S2	1	"7254720".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/04 11:08
S3	2	"6968420".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/04 11:10
S4	1	"7228440".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/04 11:11
S7	3	"7117352".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/04 11:13
S5	1	"7254716".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/04 11:15
S9	2	"6282627".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/04 11:16
S8	2	"6330670".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/04 11:56
S10	0	(micro near2 (chip\$3 circuit\$3)) with (right\$3 adj manag\$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/04 11:57
S15	32	(micro near2 (chip\$3 circuit\$3)) and (right\$3 near3 manag\$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/04 11:58

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S14	0	(micro near2 (chip\$3 circuit\$3)) same (right\$3 near3 manag\$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/04 11:58
S13	0	(micro near2 (chip\$3 circuit\$3)) same (right\$3 adj manag\$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/04 11:58
S16	24	(micro near2 (chip\$3 circuit\$3)) and (DRM)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/04 12:25
S12	0	(micro near2 (chip\$3 circuit\$3)) same (DRM)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/04 12:25
S19	1335	726/26.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/04 12:29
S21	131	(tamper near1 (proof resistan\$3)) with ((digital\$3 adj right\$3) DRM (right\$3 near3 manag\$8))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/04 12:31
S20	375	(tamper near1 (proof resistan\$3)) same ((digital\$3 adj right\$3) DRM (right\$3 near3 manag\$8))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/04 12:31
S22	131	(tamper adj (proof resistan\$3)) with ((digital\$3 adj right\$3) DRM (right\$3 near3 manag\$8))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/04 12:32
S23	25	S19 and S20	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/04 16:28
S24	181	DRM with secur\$4 with stor\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/04 16:30

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S25	13	DRM with secur\$4 with stor\$5 and 726/26.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/04 16:31
S11	0	(micro near2 (chip\$3 circuit\$3)) with (DRM)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/10 12:09
S27	111	((((integrated micro) adj (processor\$2 chip\$3 circuit\$3)) (tamper\$3 adj (proof resistant resistance))) with (DRM)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/10 12:10
S26	111	((((integrated micro) near2 (processor\$2 chip\$3 circuit\$3)) (tamper\$3 adj (proof resistant resistance))) with (DRM)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/10 12:10
S28	102	((((integrated micro) adj (processor\$2 chip\$3 circuit\$3)) (tamper\$3 adj (proof resistant resistance))) with (DRM) and (digital adj right\$3 adj management\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/10 12:16
S29	134	((((integrated micro) adj (processor\$2 chip\$3 circuit\$3)) (tamper\$3 adj (proof resistant resistance))) with (DRM (digital adj right\$3 adj management\$3)) and (digital adj right\$3 adj management\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/12 16:21
S30	93	((((integrated micro) adj (processor\$2 chip\$3 circuit\$3)) (tamper\$3 adj (proof resistant resistance))) with (LFSR)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/12 16:22
S33	49501	((((integrated micro) adj (processor\$2 chip\$3 circuit\$3)) (tamper\$3 adj (proof resistant resistance))) with (firmware\$3 programmable re\$programmable reconfigurable\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/12 16:23
S35	1346	726/26.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/12 16:25

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S40	119	S33 and S38	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/12 16:26
S39	1	S34 and S38	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/12 16:26
S38	2157	S35 S37	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/12 16:26
S37	952	726/27,31,33.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/12 16:26
S43	2	S38 and S42	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/12 16:30
S42	1563	(((integrated micro) adj (processor\$2 chip\$3 circuit\$3)) (tamper\$3 adj (proof resistant resistance))) same (reconfigurable\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/12 16:30
S34	1022	(((integrated micro) adj (processor\$2 chip\$3 circuit\$3)) (tamper\$3 adj (proof resistant resistance))) with (reconfigurable\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/12 16:30